

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit device
comprises a register circuit receives a data signal,
a delay adjustment circuit receives an output of
5 the register circuit and a driver circuit receives
an output of the delay adjustment circuit. An output
timing of the register circuit is controlled by a clock
signal. A delay time of the delay adjustment circuit
is adjusted by a delay adjustment signal based on the
10 data signal.

105250-453350